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Dated: 3/18/05

Signature: Vickie Hoeft

(Vickie Hoeft)

Docket No.: 594728804US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kim et al.

Application No.: 09/989,580

Confirmation No.: 9737

Filed: November 20, 2001

Art Unit: 2661

For: BIDIRECTIONAL BRIDGE CIRCUIT
HAVING HIGH COMMON MODE
REJECTION AND HIGH INPUT
SENSITIVITY

Examiner: Not Yet Assigned

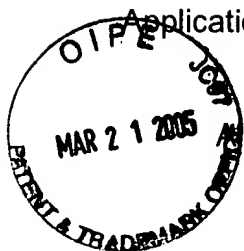
INFORMATION DISCLOSURE STATEMENT (IDS)

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits as far as is known to the undersigned (37 CFR 1.97(b)(3)).



Application No.: 09/989,580

Docket No.: 594728804US

A copy of only those references listed below is attached:

| Other Documents |
|--|
| Svelto, F. et al., "A 1.3 GHz Low-Phase Noise Fully Tunable CMOS LC VCO", IEEE Journal On Solid State Circuits, March 2000, pp. 356-361, Vol. 35, No. 3. |

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-0665, under Order No. 594728804US.

Dated: March 18, 2005

Respectfully submitted,

By

Maurice J. Pirio

Registration No.: 34,807

PERKINS COIE LLP

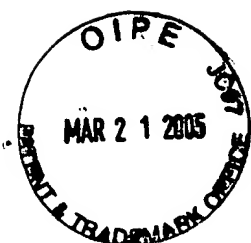
P.O. Box 1247

Seattle, Washington 98111-1247

(206) 359-8000

(206) 359-7198 (Fax)

Attorney for Applicant



PTO/SB/08a/b (08-03)

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| Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> | | | | Complete if Known | |
| | | | | Application Number | 09/989,580-Conf. #9737 |
| | | | | Filing Date | November 20, 2001 |
| | | | | First Named Inventor | Gyudong Kim |
| | | | | Art Unit | 2661 |
| | | | | Examiner Name | Not Yet Assigned |
| Sheet | 1 | of | 1 | Attorney Docket Number | 594728804US |

| U.S. PATENT DOCUMENTS | | | | | |
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| Examiner Initials* | Cite No. ¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| | | Number-Kind Code ² (if known) | | | |
| | | US-09/989,487 | | Gyudong | |
| | | US-09/989,647 | | Gyudong | |
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| Examiner Initials* | Cite No. ¹ | Foreign Patent Document | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | T ⁶ |
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| NON PATENT LITERATURE DOCUMENTS | | | |
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| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | | Svelto, F. et al., "A 1.3 GHz Low-Phase Noise Fully Tunable CMOS LC VCO", IEEE Journal On Solid State Circuits, March 2000, pp. 356-361, Vol. 35, No. 3, 2000 IEEE. | |

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| Examiner Signature | | Date Considered | |
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